Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	"3742458".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/26 14:21
L3	4348204	ep "540095"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/26 14:22
L4	1	ep-540095-$.did.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/26 14:22
S4	7	"869526"	US-PGPUB; USPAT	OR	ON	2004/11/17 11:24
S5	2577	713/200.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/17 11:25
S6	1211	713/200.ccls. and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 08:39
S7	1211	713/200.ccls. and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/17 11:26
S8	2577	(713/200).CCLS.	US-PGPUB; USPAT	OR	OFF	2004/11/17 11:26
S13	308	713/200.ccls. and @ad<"19991101" and (data near3 protect$5)	US-PGPUB; USPAT	OR	ON	2004/11/17 11:35
S15	1	713/200.ccls. and @ad<"19991101" and (data near3 protect$5) with (generat$3 with (break or interrupt))	US-PGPUB; USPAT	OR	ON	2004/11/17 11:30
S16	3	713/200.ccls. and @ad<"19991101" and (data near3 protect$5) with ("break point" or interrupt)	US-PGPUB; USPAT	OR	ON	2004/11/19 16:46
S17	66	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or interrupt))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:41
S18	0	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or interrupt)) with data adj2 protect$5	US-PGPUB; USPAT	OR	ON	2004/11/17 16:49

S19	13	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or interrupt)) and (data adj2 protect$5)	US-PGPUB; USPAT	OR	ON	2004/11/17 17:57
S20	1	"5274817".pn.	US-PGPUB; USPAT	OR	ON	2004/11/17 17:38
S21	1	"5940513".pn.	US-PGPUB; USPAT	OR	ON	2004/11/17 17:38
S23	13	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or interrupt or "debug register")) and (data adj2 protect$5)	US-PGPUB; USPAT	OR	ON	2004/11/17 17:58
S24	0	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or "debug register")) and (data adj2 protect$5)	US-PGPUB; USPAT	OR	ON	2004/11/17 18:01
S25	0	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) with ("hardware break point" or "debug register")) and (data adj2 protect$5)	US-PGPUB; USPAT	OR	ON	2004/11/17 18:02
S26	0	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) and ("hardware break point" or "debug register")) and (data adj2 protect$5)	US-PGPUB; USPAT	OR	ON	2004/11/17 18:02
S27	2	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) and ("hardware break point" or "debug register"))	US-PGPUB; USPAT	OR	ON	2004/11/17 18:02
S28	2	"6321349"	US-PGPUB; USPAT	OR	ON	2004/11/18 11:23
S29	1	"6510521".pn.	US-PGPUB; USPAT	OR	ON	2004/11/18 11:54
S30	278	("debug register" or "hardware break point")	US-PGPUB; USPAT	OR	ON	2004/11/18 11:58
S31	141	("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu)	US-PGPUB; USPAT	OR	ON	2004/11/18 11:59
S33	0	("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu) with (protect$3 adj data) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/18 11:59

S34	0	("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu) same (protect$3 adj data) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/18 11:59
S35	0	("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu) and (acess$3 adj ((protect$3 adj data) or memory)) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/18 12:00
S36	17	("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu) with (access$3) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/18 17:11
S37	0	711/163.ccls. and ("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu) with (access$3) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/18 17:12
S38	1	711/163.ccls. and ("debug register" or "hardware break point" or "hardware breakpoint" or "protected register") with (microprocessor or cpu or processor or mcu) with (access$3) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/18 17:18
S39	25	("4328542").URPN.	USPAT	OR	ON	2004/11/18 17:18
S40	34	("debug register" or "hardware break point" or "hardware breakpoint" or "protected register") with (microprocessor or cpu or processor or mcu) same (access$3 near3 (data or memory)) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:35
S41	0	"."	US-PGPUB; USPAT	OR	ON	2004/11/19 08:29
S42	0	("debug register" or "hardware break point" or "hardware breakpoint" or "protected register") with (microprocessor or cpu or processor or mcu) same (access$3 near3 (data or memory) near3 verif$5) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:01
S43	0	("debug register" or "hardware break point" or "hardware breakpoint" or "protected register") with (microprocessor or cpu or processor or mcu) same (access$3 near3 (data or memory) with verif$5) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:01

S44	103	("debug register" or "hardware break point") with (microprocessor or cpu or processor or mcu) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:06
S45	268	713/200.ccls. and @ad<"19991101" and "hardware break point" or "debug register"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:07
S46	180	713/200.ccls. and @ad<"19991101" and "hardware break point" or "debug register" and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 09:36
S48	11	("debug register" or "hardware break point" or "hardware breakpoint" or "protected register") with (microprocessor or cpu or processor or mcu) same (access$3 near3 (data or memory)) and error and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:39
S49	11	(invok$3 or report$3 or notify$3) near3 (error adj manager) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 09:41
S52	9	(invok$3 or report$3 or notify$3) near3 (error adj manager) and verification and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 16:29
S57	2	verification same (display$3 or report$3 or notify$3) with error and 713/200.ccls. and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 08:29
S60	16	((caus$4 or invok$3) near2 error near2 manager)and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 16:52
S63	31	((caus$4 or invok$3) with (error near2 manager))and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/19 16:53
S64	0	verification adj(display$3 or report$3 or notify$3) with error and 713/200.ccls. and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 08:29
S65	0	verification near3 (display$3 or report$3 or notify$3) with error and 713/200.ccls. and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 08:30
S66	4	("6052774").URPN.	USPAT	OR	ON	2004/11/22 08:34
S67	1	"5680620".pn.	US-PGPUB; USPAT	OR	ON	2004/11/22 08:38
S68	39	713/200.ccls. and @ad<"19991101" and (verif$10 with error)	US-PGPUB; USPAT	OR	ON	2004/11/22 09:07
S69	0	713/200.ccls. and @ad<"19991101" and (verif$10 with (display$3 or report$3 or notify$3) adj2 error)	US-PGPUB; USPAT	OR	ON	2004/11/22 08:42

S70	1	713/200.ccls. and @ad<"19991101" and (verif$10 with (display$3 or report$3 or notify$3) near3 error)	US-PGPUB; USPAT	OR	ON	2004/11/22 08:42
S71	5083	(verif$10 with error) and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 09:06
S72	39	(verif$10 with error) and @ad<"19991101" and 713/200.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/22 09:07
S73	96	713/200.ccls. and @ad<"19991101" and (verif$10 same error)	US-PGPUB; USPAT	OR	ON	2004/11/22 14:53
S74	0	713/200.ccls. and @ad<"19991101" and (microprocessor or cpu or processor or mcu) near3 (coupled or connected or having) near3 ("hardware break point" or "debug register" or "hardware breakpoint") and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 09:37
S75	0	713/200.ccls. and @ad<"19991101" and (microprocessor or cpu or processor or mcu) near3 (coupled or connected or having) with ("hardware break point" or "debug register" or "hardware breakpoint")	US-PGPUB; USPAT	OR	ON	2004/11/22 09:38
S76	0	713/200.ccls. and @ad<"19991101" and (microprocessor or cpu or processor or mcu) with (coupled or connected or having) with ("hardware break point" or "debug register" or "hardware breakpoint")	US-PGPUB; USPAT	OR	ON	2004/11/22 09:38
S77	0	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or "hardware breakpoint" or "debug register"))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:39
S78	0	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) near3 ("hardware break point" or "debug register" or "hardware breakpoint"))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:41
S79	2	713/200.ccls. and @ad<"19991101" and ((microprocessor or cpu or processor or mcu) with ("hardware break point" or "debug register" or "hardware breakpoint"))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:45
S80	0	713/200.ccls. and @ad<"19991101" and (generat$3 near3 (interrupt or breakpoint or "break point")) near3 ("each time" with ((data or memory) with access$3))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:48

S81	0	713/200.ccls. and @ad<"19991101" and (generat$3 near3 (interrupt or breakpoint or "break point")) near3 (("each time" or "every time" or whenever) with ((data or memory) with access$3))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:49
S82	0	713/200.ccls. and @ad<"19991101" and (generat$3 near3 (interrupt or breakpoint or "break point")) with (("each time" or "every time" or whenever) with ((data or memory) with access$3))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:49
S83	0	713/200.ccls. and @ad<"19991101" and (generat$3 with (interrupt or breakpoint or "break point")) with (("each time" or "every time" or whenever) with ((data or memory) with access$3))	US-PGPUB; USPAT	OR	ON	2004/11/22 09:50
S84	12	(generat$3 with (interrupt or breakpoint or "break point")) with (("each time" or "every time" or whenever) with ((data or memory) with access$3))and @ad<"19991101"	US-PGPUB; USPAT	OR	ON	2004/11/22 09:53
S85	0	(generat$3 with (interrupt or breakpoint or "break point")) with (("each time" or "every time" or whenever) with ((data or memory) with access$3))and 713/200.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/22 09:53
S86	1	"5940513".pn.	US-PGPUB; USPAT	OR	ON	2004/11/22 11:13
S87	1	"6169804".pn.	US-PGPUB; USPAT	OR	ON	2004/11/22 11:13
S88	1	ep-540095-$.did.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/26 11:56
S89	2	ep-735488-$.did.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 14:56
S90	0	fr-740235-$.did.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/22 14:56

| S91 | 2 | fr-2740235-$.did. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2004/11/22 14:56 |